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3/7/96



Docket: 0756-1400

Date: August 28, 1995

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Honorable Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the Rule 53(d) Patent Application of:

Inventors: Shunpei YAMAZAKI, Satoshi TERAMOTO

For: SEMICONDUCTOR CIRCUIT FOR ELECTRO-OPTICAL DEVICE  
AND METHOD OF MANUFACTURING THE SAME

Enclosed are:

[ X ] 39 pages of specification, claims and abstract

[ X ] 13 sheets of formal drawings (Figures 1(ABCD), 2(AB), 3(ABCD),  
4(AB), 5, 6, 7, 8(ABCDE), 9(ABCDE), 10, 11(ABCDE), 12(AB)  
and 13(AB))

[ ] An assignment of the invention to \_\_\_\_\_.

[ ] Declaration and Power of Attorney.

[ X ] Priority is claimed based on foreign application(s)

No(s): 6-227358, 6-230647

Filed on: August 29, 1994, August 31, 1994

Filed in: Japan

Certified copy(s) [ X ] attached

[ ] A verified statement to establish small entity status under 37 CFR 1.9  
and 37 CFR 1.27.

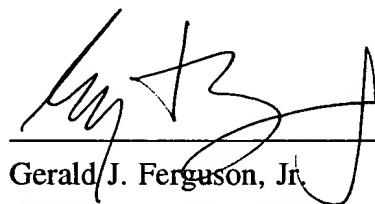
[ X ] Sheet of Filing Particulars

☐ The filing fee has been calculated as shown below.

For:	No. Filed		No. Extra	Rate Sm/Lg Entity	Fee
Basic Fee				\$365/730	
Total Claims		- 20		x 11/22	
Independent Claims		- 3		x 38/76	
First presentation of multiple dependent claims \$120/240					
Assignment Recordation Fee					
Total					

☒ NO FEE ENCLOSED.

Respectfully submitted,

  
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